Using High Performance Computing to Realize a System-Level RBDO for Military Ground Vehicles

- **David A. Lamb, Ph.D.**
- Computational Reliability and Safety Research team

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14. ABSTRACT

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The Team

TARDEC:

•David A. Lamb, Ph.D.

•Dr. David Gorsich

University of Iowa:

•Prof. K.K. Choi

•Dr. Ed Hardee

University of Maryland:

•Prof. B.D. Youn

Ghiocel Predictive Technologies:

•Dr. Dan Ghiocel

AND OTHERS

High Level View of TARDEC Reliability Modeling Efforts

TARDEC GVSL Data Collection & Lab Accreditation

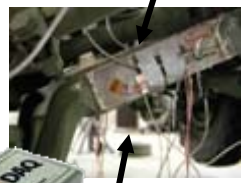
V&V

V&V

V&V

V&V

Data for improved models



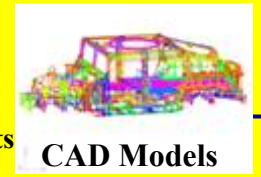
Component Data Collection

Field Data Collection and Analysis

$$P_F = \int_{y(x)<0=\mathcal{D}\subset\square_N} f_X(x)dx$$

$$E[y^l(X)] = \int_{\square_N} y^l(x)\, f_X(x)dx$$

Lead to Improvements

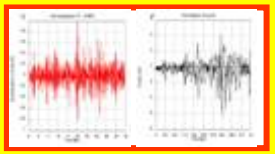


CAD Models

P.G. Dynamic Simulation

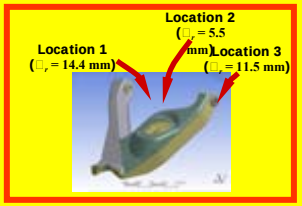
Improved Component Life via Optimization (HPC enabled)

Multi-Physics of Failure Computer-Aided Analysis & Optimization



Road Load History

Thermal Degradation Modeling



Manufacturing Defect Failure Contribution

≤ 6 *orders of magnitude* greater probability of fatigue failure at a defect

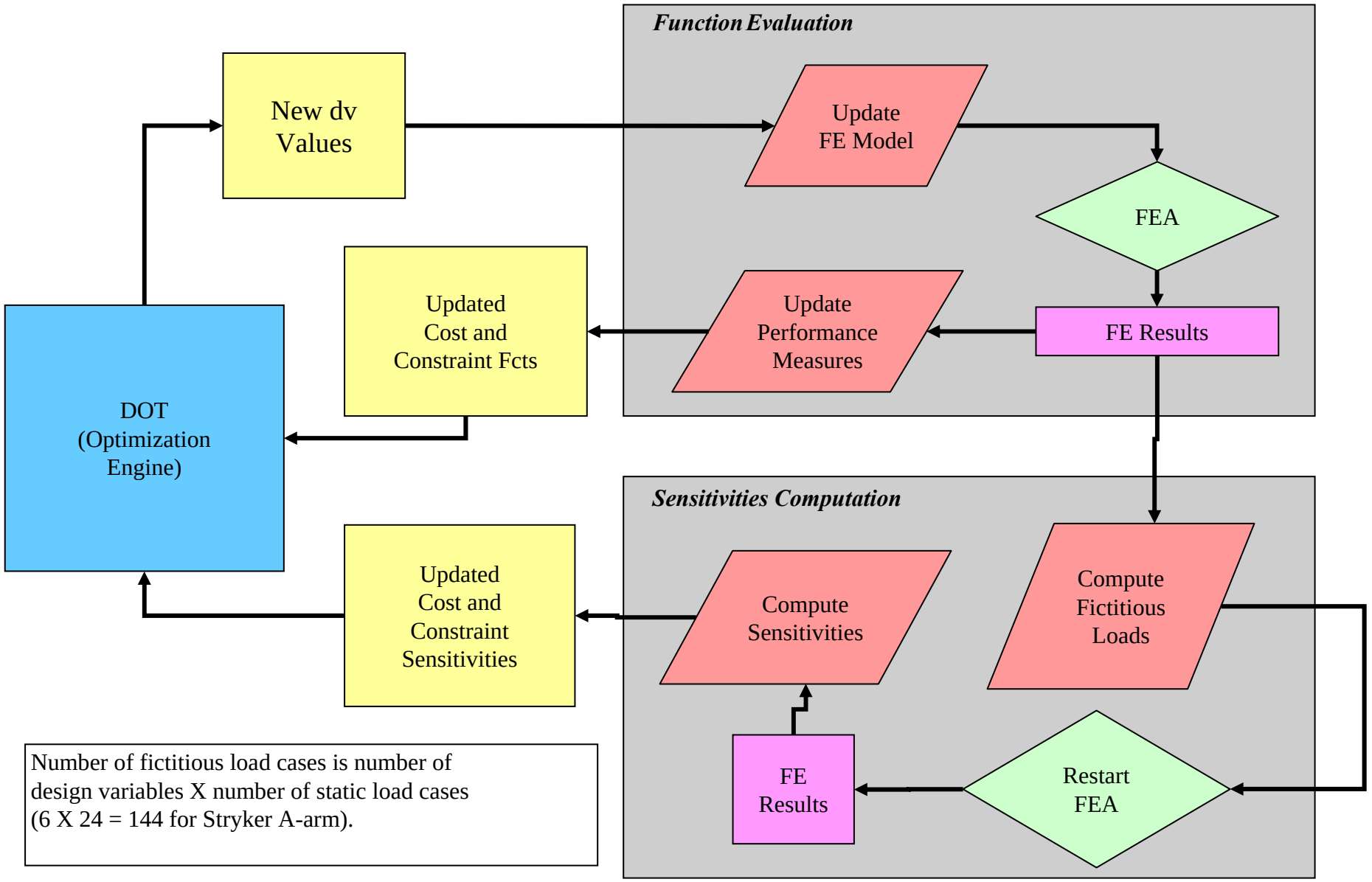
FEA/Fatigue Analysis

Some of the Other Systems Being Analyzed

Technology Developments being Leveraged from Auto Industry & ARC

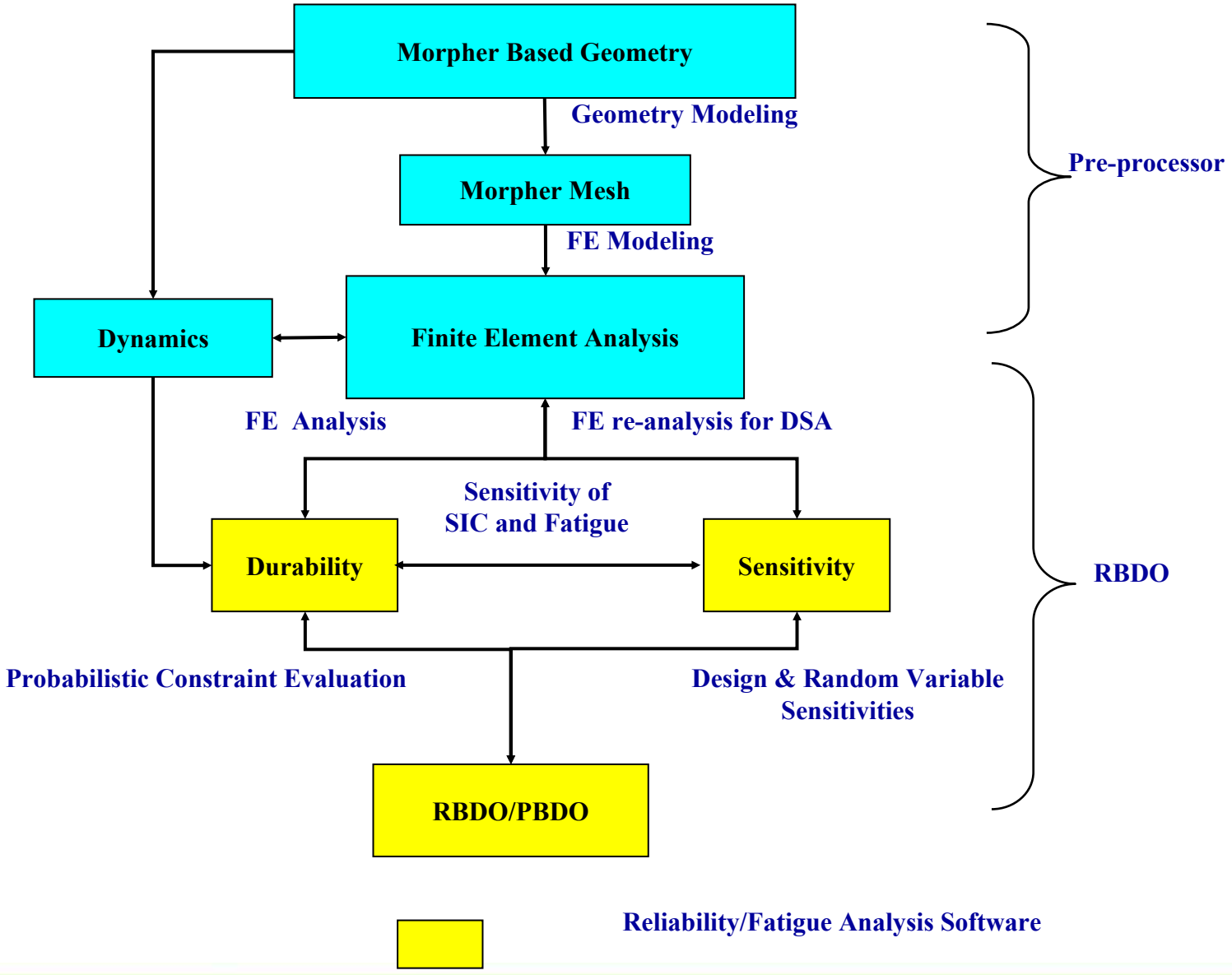
Ford nCode TOYOTA GM THE UNIVERSITY OF MICHIGAN 1817 IOWA AMSAA VEXTEC DAIMLERCHRYSLER MSC SOFTWARE LMS SAE INTERNATIONAL

The Reliability Algorithm

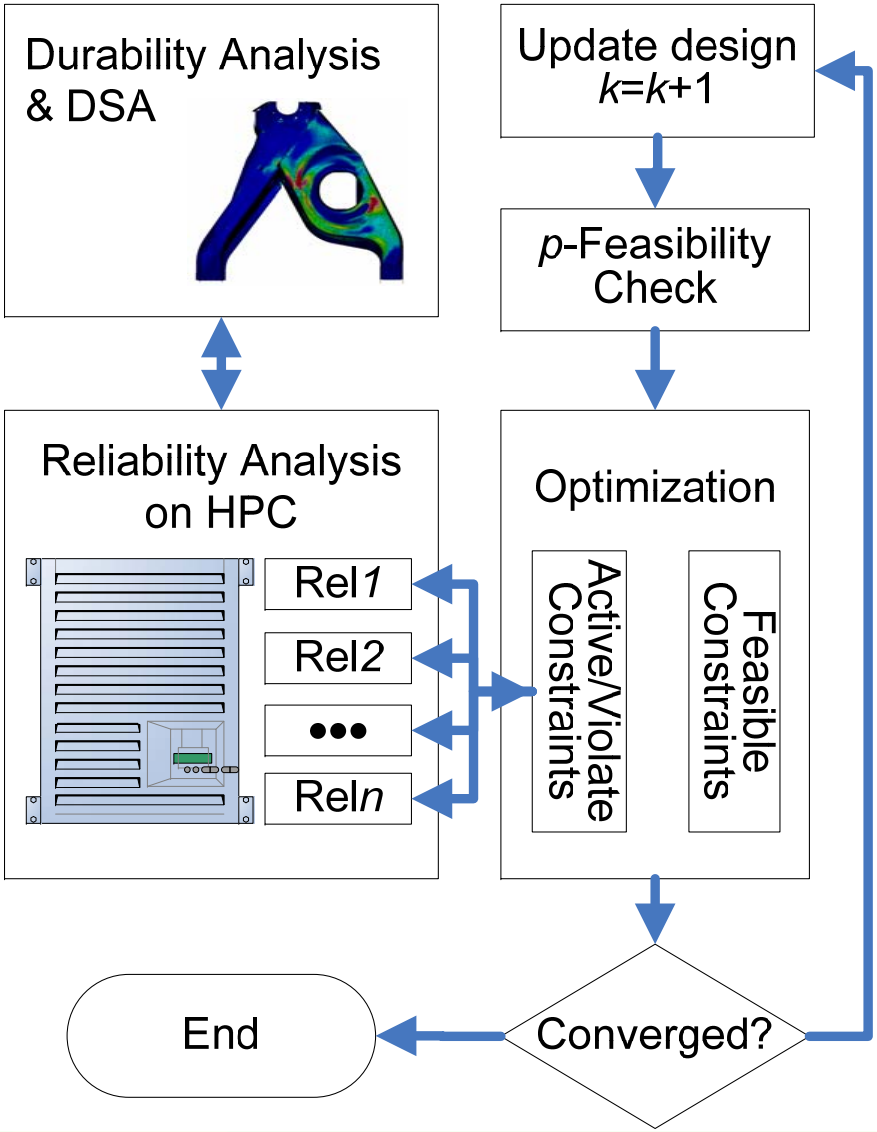


Number of fictitious load cases is number of design variables X number of static load cases (6 X 24 = 144 for Stryker A-arm).

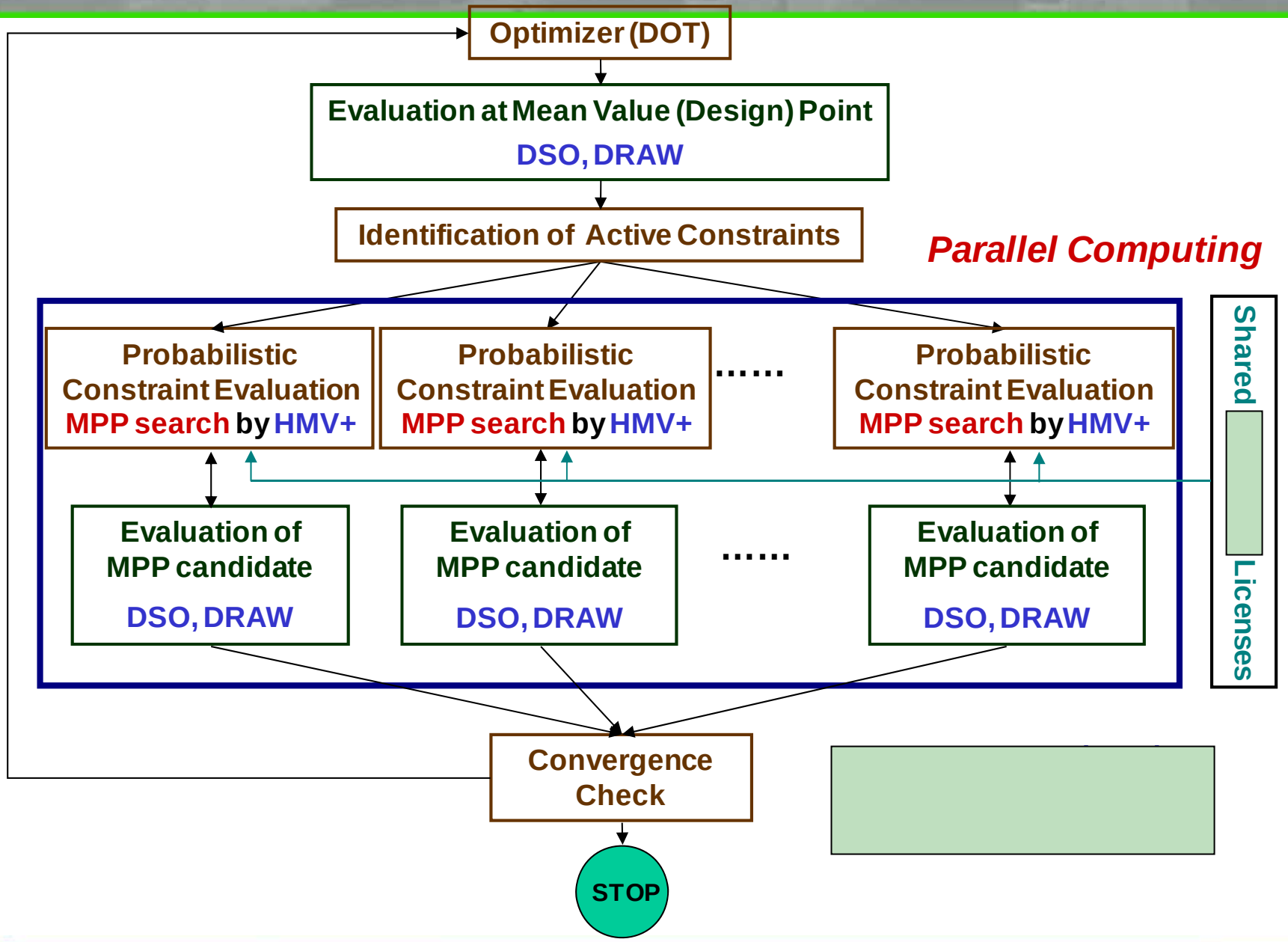
RBDO Flowchart



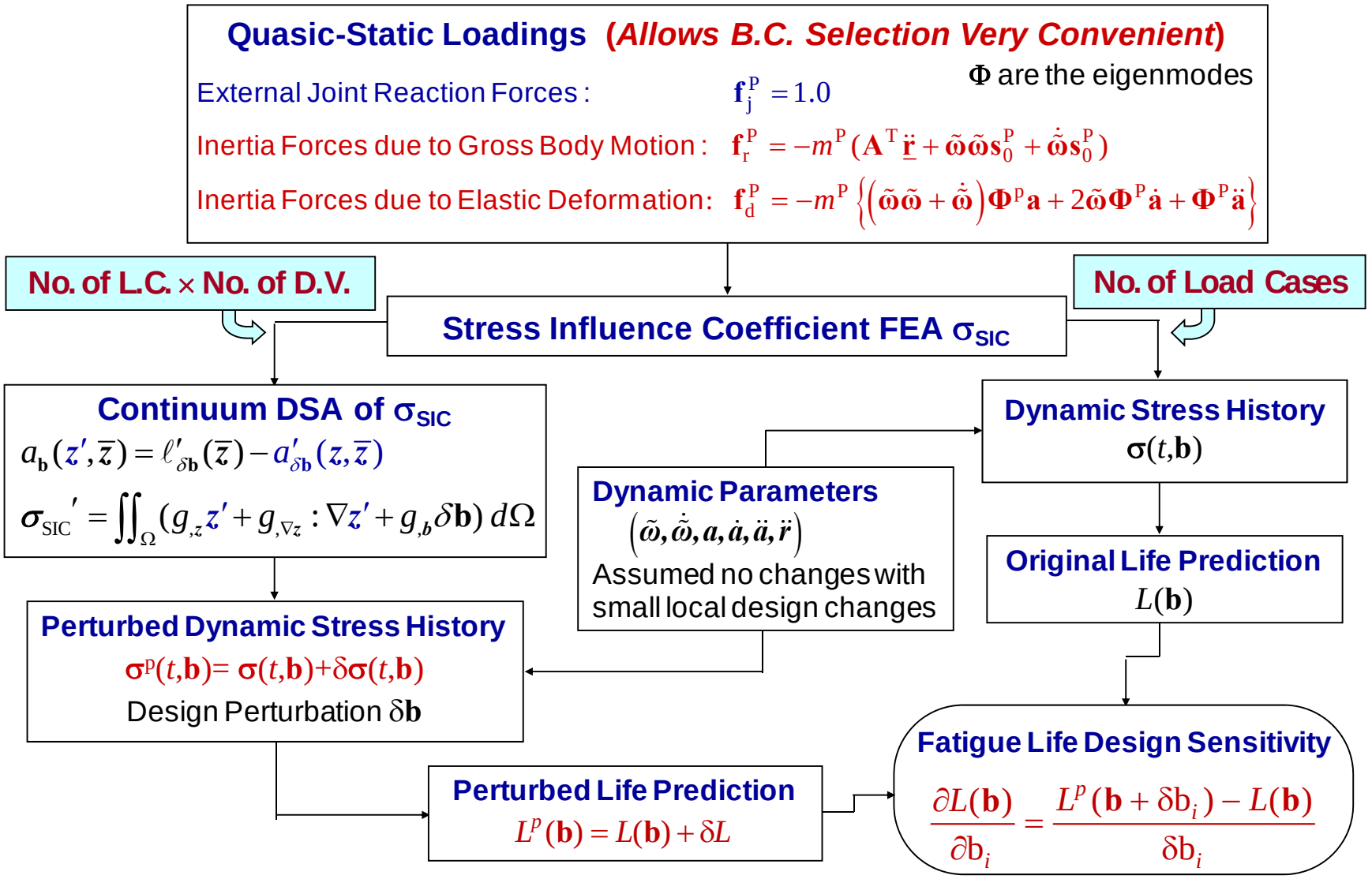
Optimization Loop



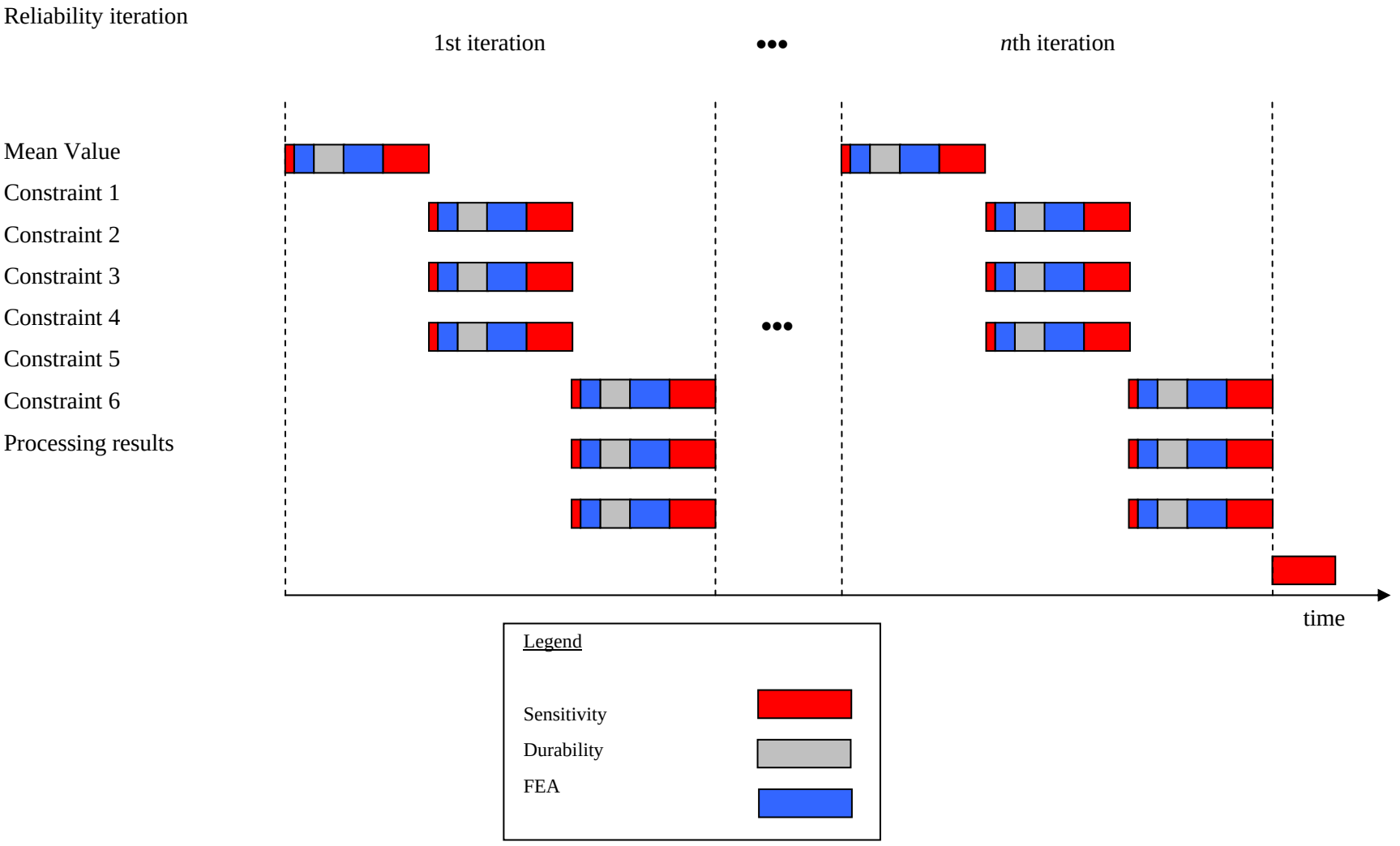
Parallel Computing for RBDO using HPC



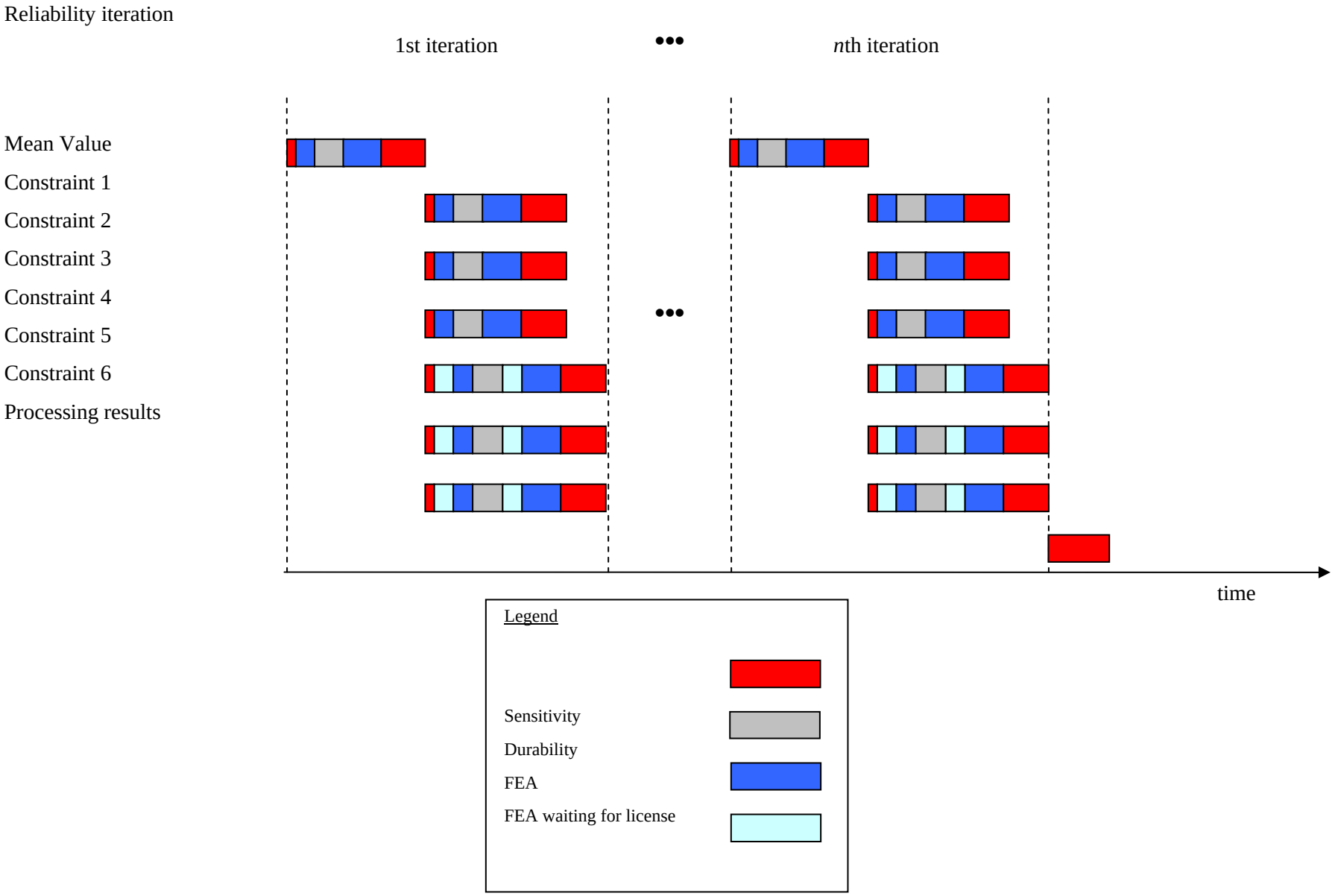
Computational Process in DRAW and DSO



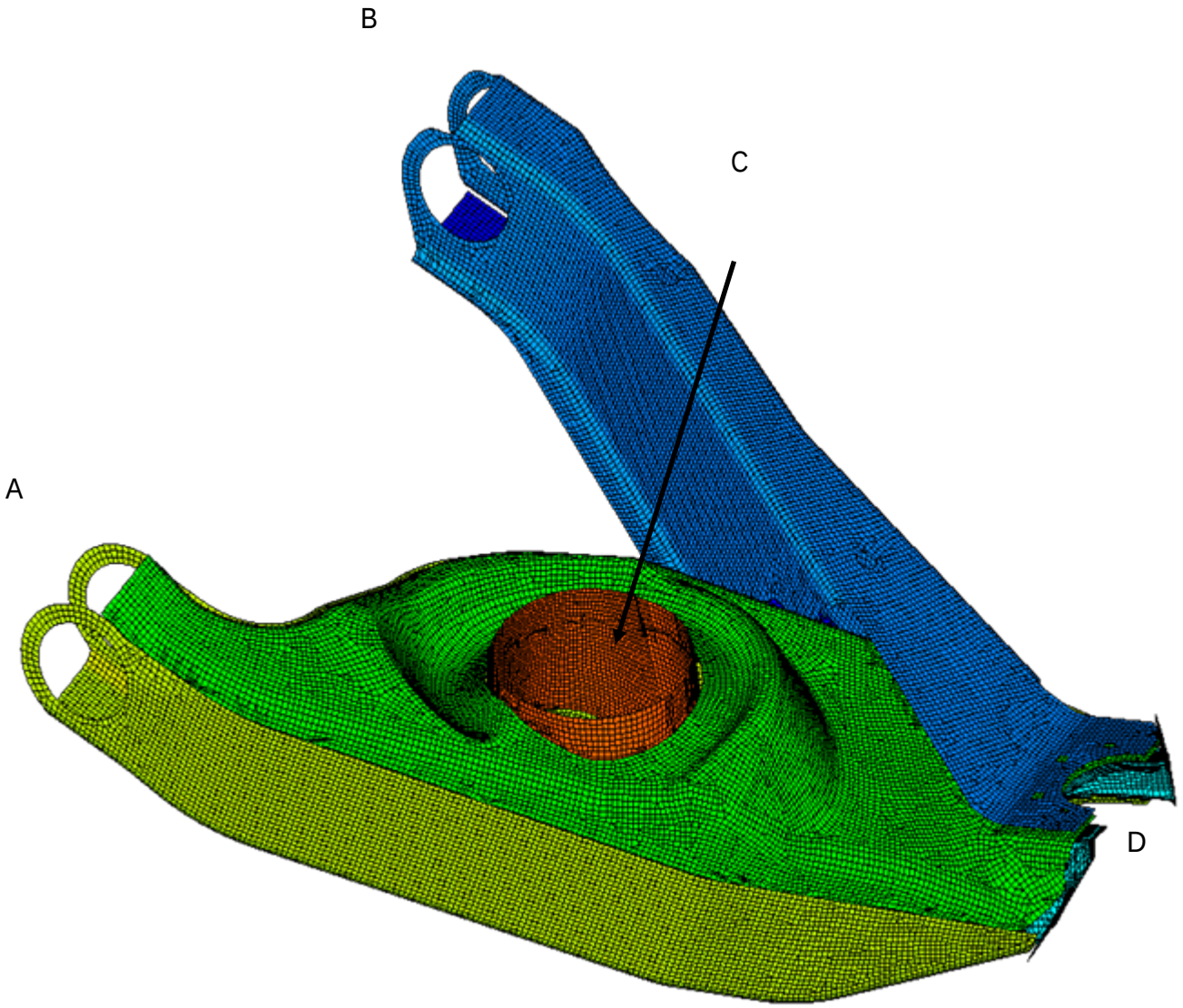
GANTT Chart for 3 Licenses



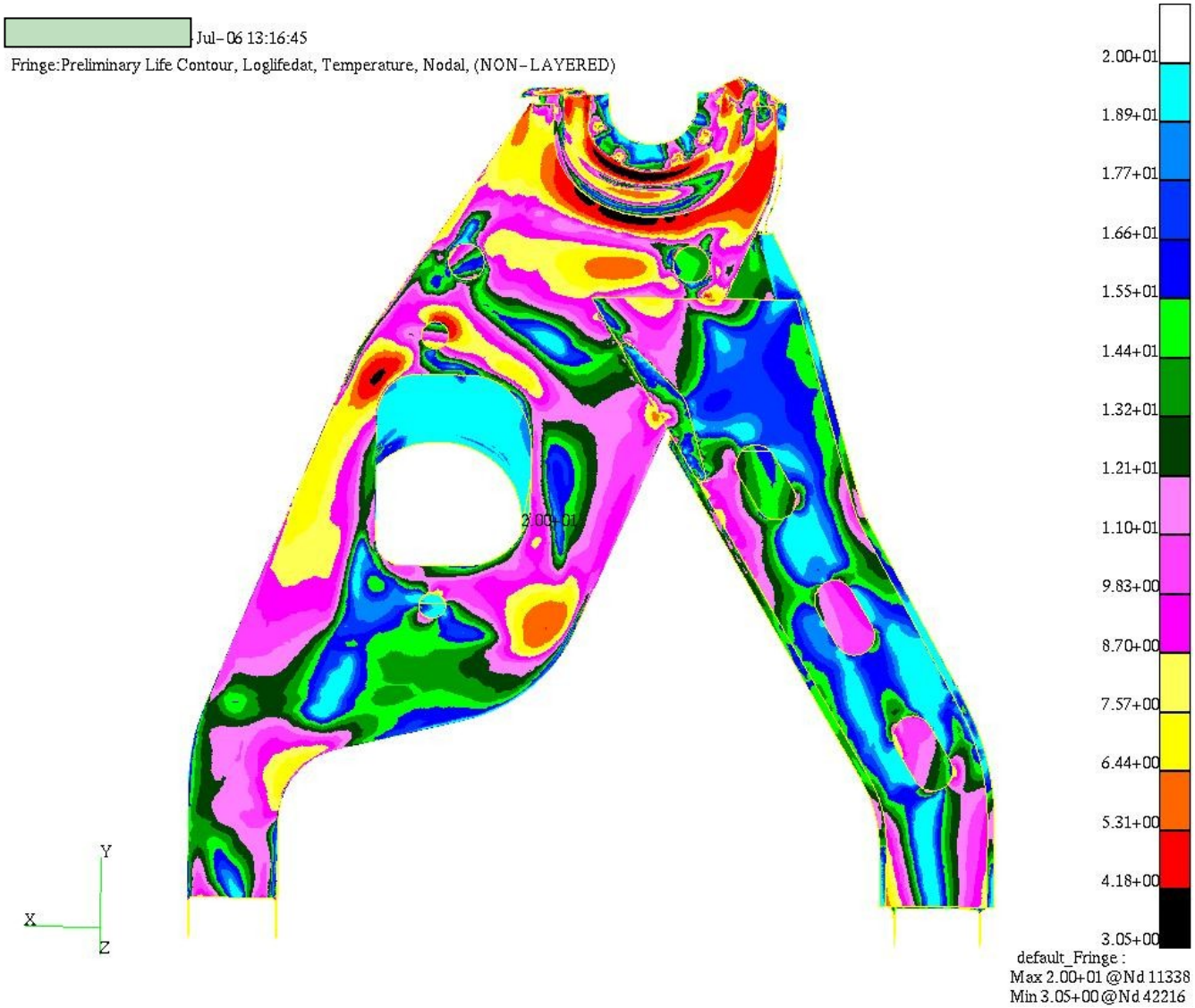
Another GANTT Chart



The Lower A-Arm



A-Arm stress plot (initial)



Reliability-Based Design Optimization Results

- The cost (volume) increased from 111.4 in^3 to 136.9 in^3.
- Fatigue life increased from 5.31 x 10^4 to 1.0 x 10^6.
- The optimization converged in 4 design iterations.
- This required 100 function evaluations, and took 1397 minutes (23.3 hours) when run in serial mode (benchmark).
- With the 16 licenses of FE solver software and using parallel execution on 16 processors, took about 206 minutes (3 hours 26 minutes).
- This was a speed-up by a factor of 6.78 over serial processing.
- Some inefficiencies still existed in the code.

Scalability Results

	Run #	No of constr.	No of licenses	No of proc.	Ave. runtime (per constraint)	Ave. idle time (per processor)	Time (PR)
Training runs	1	15	1	1	93.1	0.0	1397
	2		2	8	136.4	35.3 (282)	291
	3		4	8	125.1	23.6 (189)	259
	4		8	8	121.1	16.5 (132)	244
	5		2	15	179.1	57.6 (864)	237
	6		4	15	187.7	28.5 (428)	217
	7		8	15	191.8	13.6 (204)	206
	8		*16*	*15*	*184.9*	*17.3 (259)*	*203*
	9	30	1	1	94.1	0.0	2822
	10		2	8	126.5	53.8 (430)	529
	11		4	8	123.9	37.3 (298)	502
	12		8	8	122.4	32.3 (258)	492
	13		2	15	176.7	65.3 (979)	419
	14		4	15	170.9	33.2 (498)	376
	15		8	15	168.6	15.9 (239)	354
	16		*16*	*15*	*165.7*	*14.0 (210)*	*346*
	17	30	2	30	324.2	122.8 (3684)	448
	18		4	30	330.1	63.6 (1909)	395
	19		8	30	339.9	41.2 (1236)	382
	20		16	30	340.8	30.0 (901)	372
Test runs	21	15	7	10	125.7	53.2 (532)	242
	22	30	15	20	190.9	64.5 (1289)	352

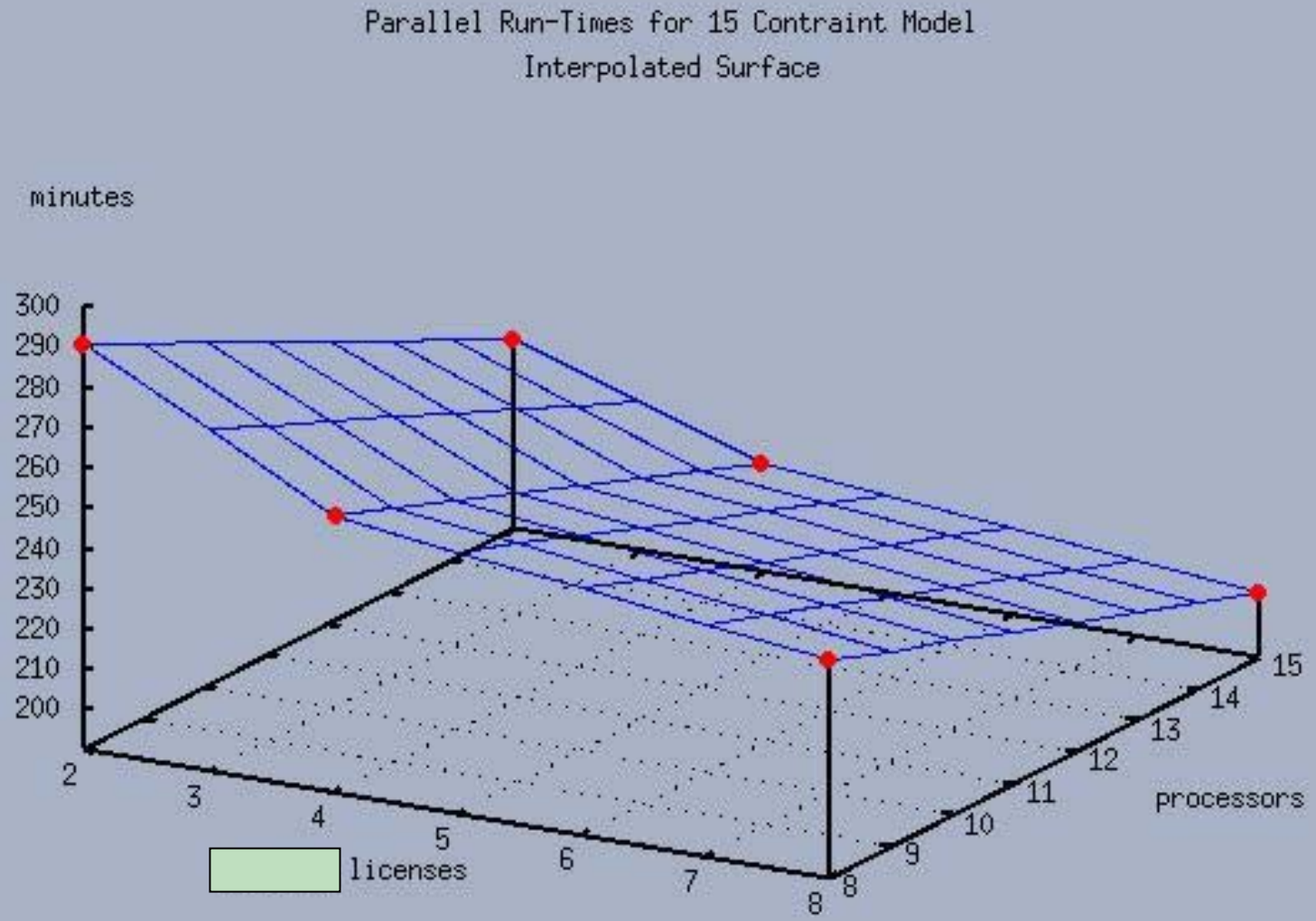
Legend for Defining Runtimes

- For:
- PR = parallel runtime in real time
- CR = total computational runtime, summed up over the processors
- I = total idle time, summed up over the processors
- np = number of processors
- nc = number of constraints
- we have:
- PR = (CR + I) / np
- or:
- PR = (CR / nc) * (nc / np) + I / np
- That is,
- parallel runtime in real time = (ave. computational runtime)*(ratio of constraints to processors) + ave. processor idle time

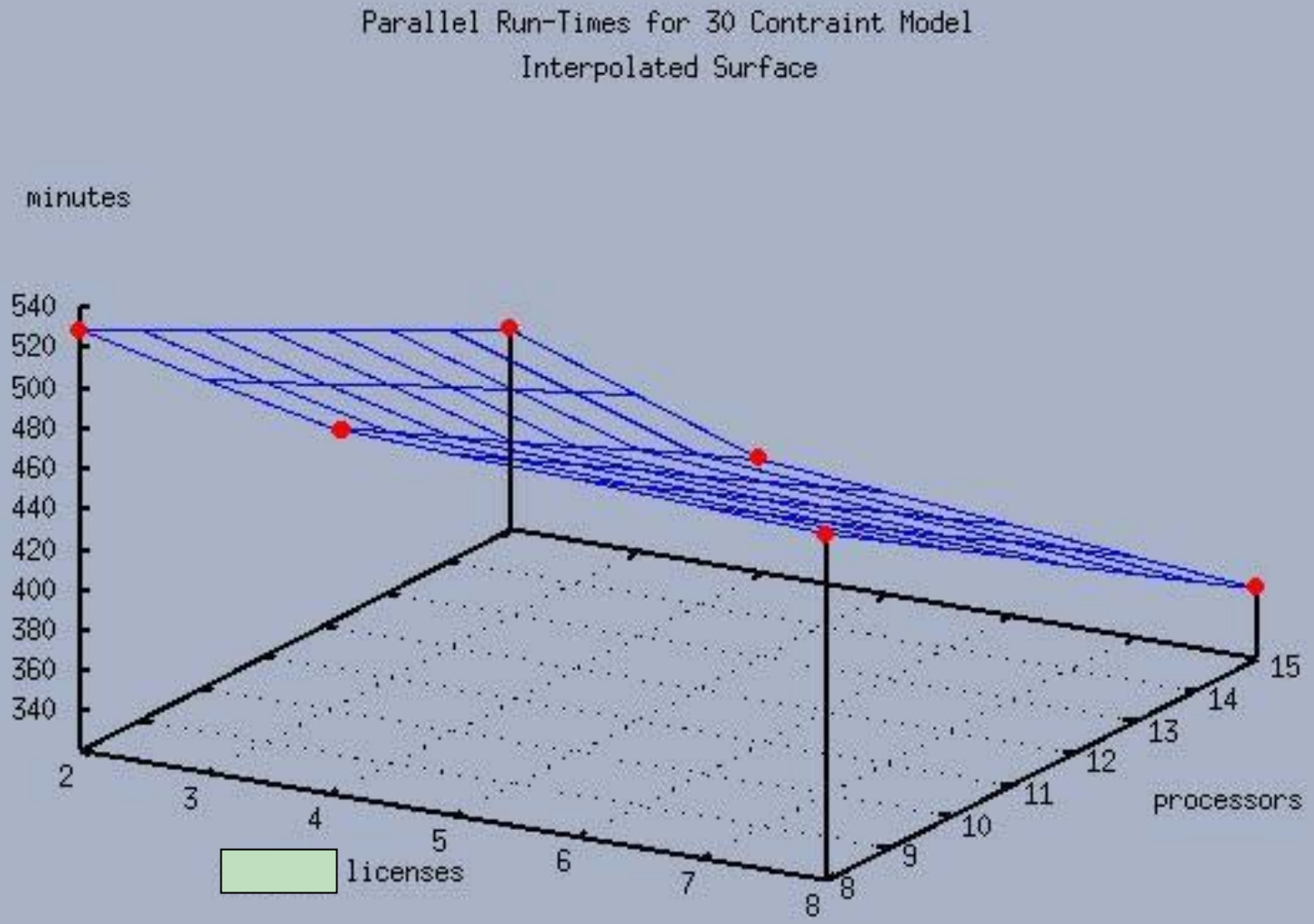
Trends observed in pilot runs

- The following trends appear significant from the data:
- Increasing the number of licenses decreases the average idle time per processor.
- Increasing the number of processors increases the average computational runtime per constraint.
- Increasing the number of licenses decreases the average runtime per constraint (when np<nc) and increases the average runtime per constraint (when np=nc.)
- Increasing the number of processors decreases the average idle time per processor if the number of licenses is 8 and increases the average idle time per processor if the number of licenses is 2, with no consistent trend when the number of licenses is 4.

Scalabiltiy Surface



Scalability Surface



Many Challenges

- Configuring number of licenses, processors, constraints
  - More processors than licenses?
  - One processor per constraint?
  - How does this scale?
- Memory and I/O problems
  - We had unexplained Scratch/Swap memory overutilization
  - I/O has been a constant issue
  - “Supercomputer – (definition) a devise for transforming a compute bound problem into an I/O bound problem”
- Cost of licenses
  - We must get better ‘package’ pricing for massively parallel runs from COTS software suppliers
  - Or, we must instead use “home-grown” code

Conclusions

- Follow-on project to start in August/September time frame
- More processors (over 100), More FEA licenses (32)
- Multi-component